



Attorney Docket No. 42390P1674C2D4

RECEIVED

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TC 2800 MAIL ROOM

In re Application of:

Pippin, J.

Group Art Unit: Not yet assigned

Application No.: 09/707,486

Examiner: Not yet assigned

Filed: November 7, 2000

For: Temperature-Based Clock Frequency
Controller Apparatus and Method

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service on the date shown with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 3/19/01.

(Signature)
(signed)

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, the references listed on the enclosed Form PTO-1449 are submitted for consideration by the Examiner in the examination of the above-identified patent application.

The full consideration of the references in their entirety by the Examiner is respectfully requested and encouraged. Also, it is respectfully requested that the references be entered into the record of the present application and that the Examiner place his or her initials in the appropriate area on the enclosed Form PTO-1449, thereby indicating the Examiner's consideration of each of the references.

The submission of the references listed on the Form PTO-1449 is for the purpose of providing a complete record and is not a concession that the references listed thereon are prior art to the invention claimed in the patent application. The right is expressly reserved to establish an invention date earlier than the above-identified filing date in order to remove any reference submitted herewith as prior art should it be deemed appropriate to do so.

Further, the submission of the references is not to be taken as a concession that any reference represents art that is relevant or analogous to the claimed invention. Accordingly, the right to argue that any reference is not properly within the scope of prior art relevant to an examination of the claims in the above-identified application is also expressly reserved.

The Information Disclosure Statement is being filed:

- ☒ (a) within three months of the filing date of the patent application, (b) within three months of the date of entry into the national stage as set forth in 37 C.F.R. § 1.491 of the international application, or (c) before the mailing date of a first Office Action on the merits.
- ☐ after (a), (b), or (c) above, but before the mailing date of a final action under 37 C.F.R. § 1.113 or a Notice of Allowance under 37 C.F.R. § 1.311, and includes:
- ☐ the fee of \$180 set forth in 37 C.F.R. § 1.17(p) (see "Fees" below).
- ☐ after the mailing date of a final action under 37 C.F.R. § 1.113 or a Notice of Allowance under 37 C.F.R. § 1.311, and includes the Certification under 37 C.F.R. § 1.97(e) (see "Certification" below), a Petition requesting consideration of the Information Disclosure Statement (see "Petition" below), and the Petition Fee set forth in 37 C.F.R. § 1.17(i) (see "Fees" below).

Copies Of The References

- ☐ Copies of certain references listed on the enclosed Form PTO-1449 are enclosed herewith. Attached to each reference not in the English language is a concise explanation of the relevance pursuant to 37 C.F.R. § 1.98(a)(3).
- ☐ A copy of the foreign search report is enclosed herewith.
- ☒ All references listed on the enclosed Form PTO-1449 were previously identified in the parent application of the present application, and copies of the references were furnished at that time. Accordingly, additional copies of the references are not submitted herewith, so as not to burden the file with duplicate copies of references. The Examiner is respectfully requested to carefully review the references in accordance with the requirements set out in the Manual of Patent Examining Procedure. In accordance with 37 C.F.R. § 1.98(d), the details of the parent application(s) relied upon for an earlier filing date under 35 U.S.C. § 120 in which copies of the references were previously furnished are set out below:

| U.S. APPLICATIONS | | Status (check one) | | |
|-------------------|------------------|--------------------|---------|-----------|
| U.S. APPLICATIONS | U.S. FILING DATE | PATENTED | PENDING | ABANDONED |
| 1. 09/093,988 | 6/8/98 | | X | |

Other Applications

☒ The Examiner's attention is directed to the following U.S. patent application(s):

| U.S. APPLICATIONS | | Status (check one) | | |
|-------------------|------------------|--------------------|---------|-----------|
| U.S. APPLICATIONS | U.S. FILING DATE | PATENTED | PENDING | ABANDONED |
| 1. 08/124,980 | 9/21/93 | | | X |
| 2. 08/401,473 | 3/9/95 | | | X |
| 3. 08/636,024 | 4/19/96 | | X | |
| 4. 08/660,016 | 6/6/96 | X | | |
| 5. 09/281,237 | 3/30/99 | | X | |
| 6. 09/281,632 | 3/30/99 | | X | |
| 7. 09/707,448 | 11/7/00 | | X | |
| 8. 09/707,860 | 11/7/00 | | X | |
| 9. 09/707,386 | 14/7/00 | | X | |

Fees

- ☒ No fee is owed by the applicant(s).
☐ The IDS Fee of \$180.00 under 37 C.F.R. § 1.17(p) is enclosed herewith.

Method Of Payment Of Fees

- ☐ Attached is a check in the amount of \$ 180.00.
☐ Charge Deposit Account No. 02-2666 in the amount of \$. (A duplicate copy of this communication is enclosed for that purpose.)

Authorization To Charge Additional Fees

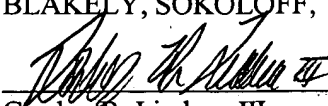
- ☒ If any additional fees are owed in connection with this communication, please charge Deposit Account No. 02-2666. (A duplicate copy of this communication is enclosed for that purpose.)

Instructions As To Overpayment

- ☒ Credit Account No. 02-2666.
☐ Refund

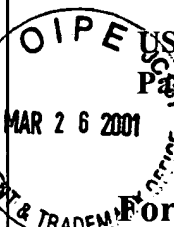
Date: March 13, 2001

Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


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Docket No.: 42390P1674C2D4
Application No.: 09/707,486

| | | |
|---|--|---------------------------------------|
|  US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified) | Atty. Docket No.: 042390.P1674C2D4 | Application No.: 09/707,486 |
| | Applicant: Pippin, J. | |
| | Filing Date: November 7, 2000 | |

US Patent Documents

| Examiner's Initials | Date | Document Number | Name | Class | Sub- Class | Filing Date |
|------------------------|------|--------------------|------------------------|-------|---------------|-------------|
| | | 4,432,031 | Premarlani | 361 | 97 | |
| | | 4,442,972 | Sahay et al. | 236 | 1EA | |
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| | | 5,291,607 | Ristic et al. | 395 | 750 | |
| | | 4,789,819 | Nelson | 323 | 314 | |
| | | 5,287,292 | Kennefal | 364 | 550 | |
| | | 5,255,149 | Matsuo | 361 | 103 | |
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| | | 5,253,938 | Stixrud | 374 | 173 | |
| | | 4,591,855 | Blackburn | 340 | 870.13 | |
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| | | 4,787,007 | Matsumura et al. | 364 | 98 | |
| | | 4,903,106 | Fukunaga et al. | 357 | 43 | |
| Examiner | | | Date Considered | | | |

-----Examiner:--Initial-if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

| | | | | | |
|--|--|--|--|---------------------------------------|--|
| US Department of Commerce Patent and Trademark Office | | Atty. Docket No.: 042390.P1674C2D4 | | Application No.: 09/707,486 | |
| | | Applicant: Pippin, J. | | | |
| | | Filing Date: November 7, 2000 | | | |

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PTO-1449 (Modified)

| US Patent Documents | | | | | | | |
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| Examiner's Initials | Date | Document Number | Name | Class | Sub-Class | Filing Date | |
| | | 4,935,864 | Schmidt et al. | 363 | 141 | | |
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| | | 5,325,256 | Weng et al. | 364 | 557 | | |
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| | Chin, Shu-Yuan and Chung-Yu Wu, "A New Type of Curvature-Compensated CMOS Bandgap Voltage References, VLSITSA, 1991, pps. 398-402. |
| | Ferro, Marco, Franco Salerno and Rinaldo Castello, "A Floating CMOS Bandgap Voltage Reference for Differential Applications", IEEE: Journal of Solid-State Circuits, Vol. 24, No. 3, June 1989, pps. 690-697. |
| | Salminen, O. and K. Halonen, "The Higher Order Temperature Compensation of Bandgap Voltage References", IEEE, 1992, pps. 1388-1391. |
| | Allen and Holberg, "CMOS Analog Circuit Design", H.P.W., pp. 539-549, 1987. |
| | Analog Devices, Low Power, Programmable Temperature Controller, pps. 1-16, 1995 |
| | Sanchez et al., "Thermal Management System for High Performance PowerPC Microprocessors", pps. 325-330, 1997. |
| Examiner | Date Considered |

Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant



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|--|-------------|--|----------------------|---------------------------------------|-----------------------|------------------------|
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| | | Applicant: Pippin, J. | | | | |
| | | Filing Date: November 7, 2000 | | | | |
| | | | | | | |
| Examiner's Initials | Date | Document Number | Name | Class | Sub- Class | Filing Date |
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| | | 5,490,059 | Mahalingaiah, et al. | 364 | 166 | |
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| | | 5,485,127 | Bertoluzzi et al. | 331 | 69 | |
| | | 4,893,097 | Zwack | 331 | 176 | |
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| | | 0683558A1 | EPO | | | |
| | | | | | | |
| Date Considered | | | | | | |

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